

Resolving Capacitor Discrepancies Between Large and Small Signal FET Models

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A novel solution is presented for the well known capacitor discrepancy problem between large and small signal FET models. The discrepancy arises due to the two-parameter bias voltage dependence of the intrinsic FET model capacitances. The resolution is enabled by the proper choice of partial-integration constants associated with the transformation of a charge source in the large signal model to a capacitor in the small signal model.

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